

Application/Control Number: 10/517,106

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Art Unit: 2832

**EXAMINER'S AMENDMENT**

An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.

Authorization for this examiner's amendment was given in a telephone interview with Robert Crawford [32,122].

The application has been amended as follows:

✓ In claim 1, line 2, before "resistor sub-bodies", insert -- elongated --.

✓ In claim 1, line 4, before "two of the columns", insert -- and along --.

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In claim 1, line 8, delete "wherein each tap of the columns of taps is configured for outputting an output potential via a contact area which is connected with the concerning tap.",  
and insert -- at least two taps in one of the plurality of column of taps are configured to provide respectfully different output potentials due to respective locations at which said at least two taps connect to the one or more of the resistor sub-bodies.—

IDC-A1,AMD

✓ In claim 3, line 1, delete "characterised", and insert -- characterized --.

✓ In claim 4, line 1, delete "characterised", and insert -- characterized --.

✓ In claim 5, line 1, delete "characterised", and insert -- characterized --.

✓ In claim 6, line 1, delete "characterised", and insert -- characterized --.

✓ In claim 7, line 2, delete "characterised", and insert -- characterized --.

✓ In claim 7, line 4, before "resistor sub-bodies", insert -- elongated --.

✓ In claim 7, line 5, after "resistor sub-bodies situated between", insert – and along --.

IDC-A2,AMD

In claim 7, line 6, after "each of the taps of the column of taps," insert – with electrical connection between respective sub-bodies being exclusively via the taps connected to the resistor sub-bodies --.

*D412alt*

In claim 7, line <sup>9</sup>~~11~~, delete "wherein each tap of the column of taps is configured for outputting an output potential via a contact area which is connected with the concerning tap, and wherein electrical connection between the resistor sub-bodies are exclusively via the taps connected with the resistor sub-bodies." and insert -- and each tap of the column of taps is

IDC-A3,AMD

configured for outputting an output potential. --.



In claim 8, line 2, delete "characterised", and insert -- characterized --.

In claim 8, line 4, before "resistor sub-bodies", insert -- elongated --.

In claim 8, line 5, after "resistor sub-bodies situated between", insert – and along --.

IDC-A4,AMD

In claim 8, line 6, after "each of the taps of the column of taps," insert – with electrical connection between respective sub-bodies being exclusively via the taps connected to the resistor sub-bodies --.

*D412alt*

In claim 8, line <sup>9</sup>~~11~~, delete "wherein each tap of the column of taps is configured for outputting an output potential via a contact area which is connected with the concerning tap, and wherein electrical connection between the resistor sub-bodies are exclusively via the taps connected with the resistor sub-bodies." and insert -- and each tap of the column of taps is

IDC-A5,AMD

configured for outputting an output potential. --.



In claim 12, line 2, before "resistor sub-bodies", insert -- elongated --.

In claim 12, line 3, after "resistor sub-bodies situated between", insert – and along --.